

A1  
an air interface to mobile station 60 which passes the received data to a data receiver 70 hosting an application using TCP. The delay can be inserted into the communication channel at either base station 40 or mobile station 60. In base station 40, data is transmitted and received via RF section 80. Channel delay can be inserted into data being transmitted by base station 40 or into acknowledges received by base station 40. Delays may be inserted data transmitted by base station 40 using buffer 82. Buffer 82 may be a shift register or cyclically addressed memory. Processor 84 controls the delay by controlling the number of stages the data must pass through when passing through buffer 82. Processor 84 monitors the channel delay by monitoring acknowledge messages received from RF section 80. As a result, processor 84 can modify the depth or amount of delay added by buffer 82 until the desirable delay is measured as seen by the delay in acknowledges received in response to data transmissions.

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**IN THE CLAIMS**

Please cancel claim 4 without prejudice or disclaimer to the subject matter contained therein.